

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1	display and horizontal scanner and shift register and monitor circuit and scanning and inverse clock signal and control circuit and synchronization and sampling signal and shift pulse	US-PGPUB	ADJ	ON	2006/09/29 12:43

*Interference Search History
Printout*

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	display and horizontal scanner and shift register and monitor circuit and scanning and inverse clock signal and control circuit and sample near1 hold pulse and synchronization and sampling signal and shift pulse	US-PGPUB	ADJ	ON	2006/09/29 12:43
L2	0	display and horizontal scanner and shift register and scanning and inverse clock signal and control circuit and sample near1 hold pulse and synchronization and sampling signal and shift pulse	US-PGPUB	ADJ	ON	2006/09/29 12:43
L3	1	display and horizontal scanner and shift register and monitor circuit and scanning and inverse clock signal and control circuit and synchronization and sampling signal and shift pulse	US-PGPUB	ADJ	ON	2006/09/29 12:48
L4	385	display and horizontal scanner	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:45
L5	1	display and horizontal scanner and shift register and monitor circuit and scanning	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:46
L6	1	display and horizontal scanner and shift register and scan\$4 and control\$6 and sampl\$6 signal	US-PGPUB	ADJ	ON	2006/09/29 12:47
L7	3	display and horizontal scanner and shift register and scan\$4 and control\$6 and sampl\$6 signal	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:49
L8	1	display and horizontal scanner and shift register and monitor circuit and scanning and inverse clock signal and control circuit and synchronization and sampling signal and shift pulse	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:51

EAST Search History

L9	1	display and horizontal scanner and shift register and scan\$4 and control\$6 and sampl\$6 signal and inverse clock signal	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:49
L10	1	display and horizontal scanner and shift register and scan\$4 and control\$6 and sampl\$6 signal and inverse clock signal and synchronization	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:50
L11	43	display and horizontal scanner and shift register and scan\$4 and control\$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:49
L12	3	display and horizontal scanner and shift register and scan\$4 and control\$6 and sampl\$6 signal	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:50
L13	1	display and horizontal scanner and shift register and scan\$4 and control\$6 and sampl\$6 signal and inverse clock signal	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:50
L14	1	display and horizontal scanner and shift register and scan\$4 and control\$6 and sampl\$6 signal and inverse clock signal inverse and synchronization	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:51
L15	1	display and horizontal scanner and shift register and monitor circuit and scanning and inverse clock signal and control circuit and synchronization and sampling signal and shift pulse	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 12:51